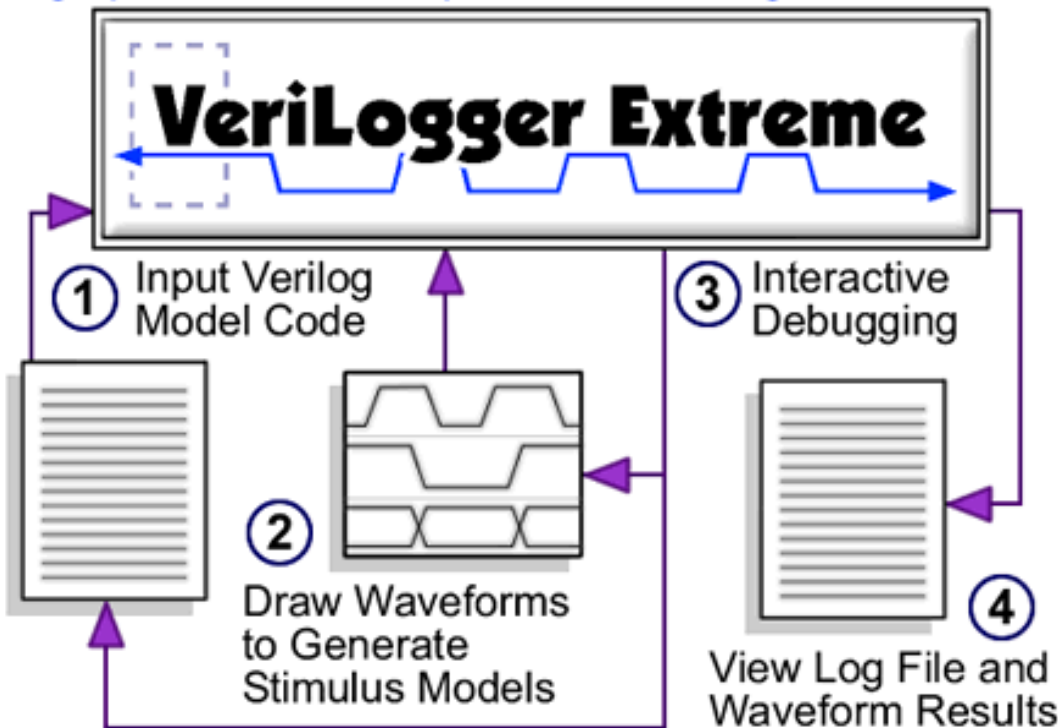


VeriLogger Extrem Version 19

High-performance compiled-code Verilog 2001 simulator



High-performance compiled-code Verilog 2001 simulator

VeriLoggerExtrem unterstützt jetzt zunehmend Verilog 2005 Features und jetzt auch **System Verilog**.

Hier die neuen Features in Version 19:

VeriLogger (System Verilog Simulator)

Compilation Enhancements:

- Support for MingWin C++ compiler
- Easy swapping between 32/64bit simulations on Windows and Linux

Performance Improvements:

- Extended elaboration optimizations to support array types (more than 2x speedup in some benchmarks)
- Simplified simulator code generation to speed up simulation compile and run times
- Faster compile times and smaller memory footprint for time-annotated (SDF) simulations

New SystemVerilog Types:

- Dynamic container types: associate array, dynamic array, queue, string
- Multi-dimensional packed arrays
- Classes and Class Handles (including generic classes)
- Named event arrays

New SystemVerilog Expressions/Statements:

- unpacked array slices
- foreach loop
- unique-case and unique-if
- rand-case
- wildcard equality operators ==? and !=?
- binary logical operators -> and <->
- "inside" operator
- static and dynamic cast operators
- inc/dec assignment operators
- immediate assertions
- void cast
- subprogram argument passing as reference

New Systemtask Functionality:

- Math systemtasks
- %p format in \$display-like system tasks

New Preprocessor Features:

- Support ``include <>`
- Directive nesting in macro definitions
- Token pasting operator

Damit Anwender nicht die berühmte Katze-im-Sack kaufen müssen, besteht für Interessenten die Möglichkeit, die Simulations-Software zusammen mit BugHunter Pro – der GUI für den Simulator und dem Debugging Tool – über einen Zeitraum von 6 Monaten zu testen und zu nutzen.