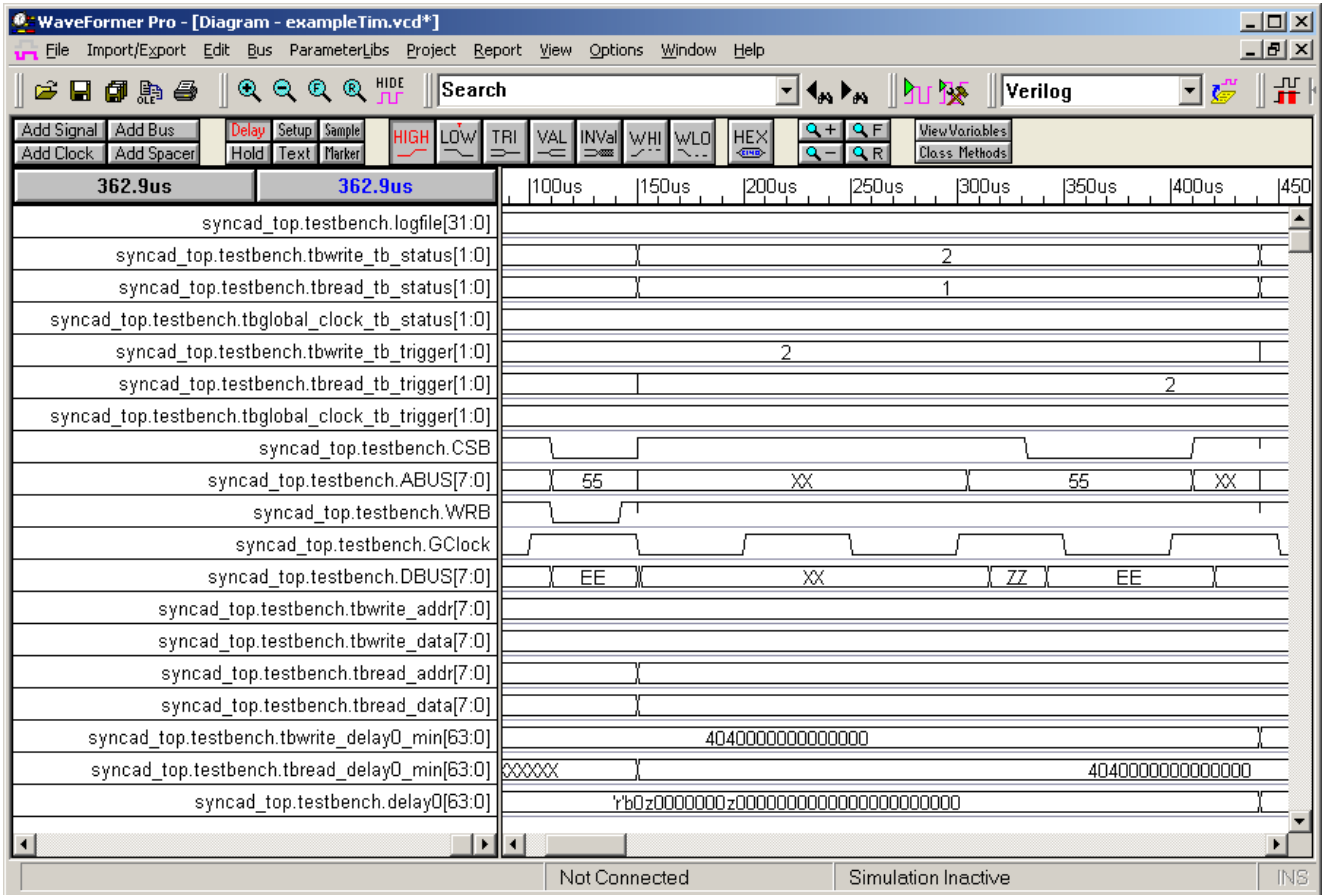


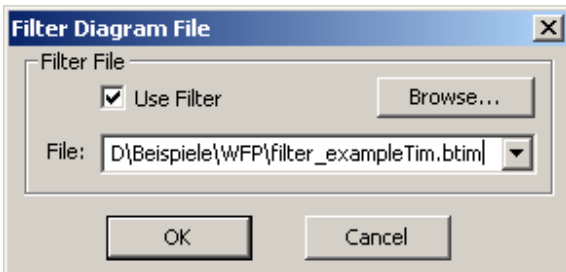
Import von externen Dateien

Beispiel: VCD-Datei

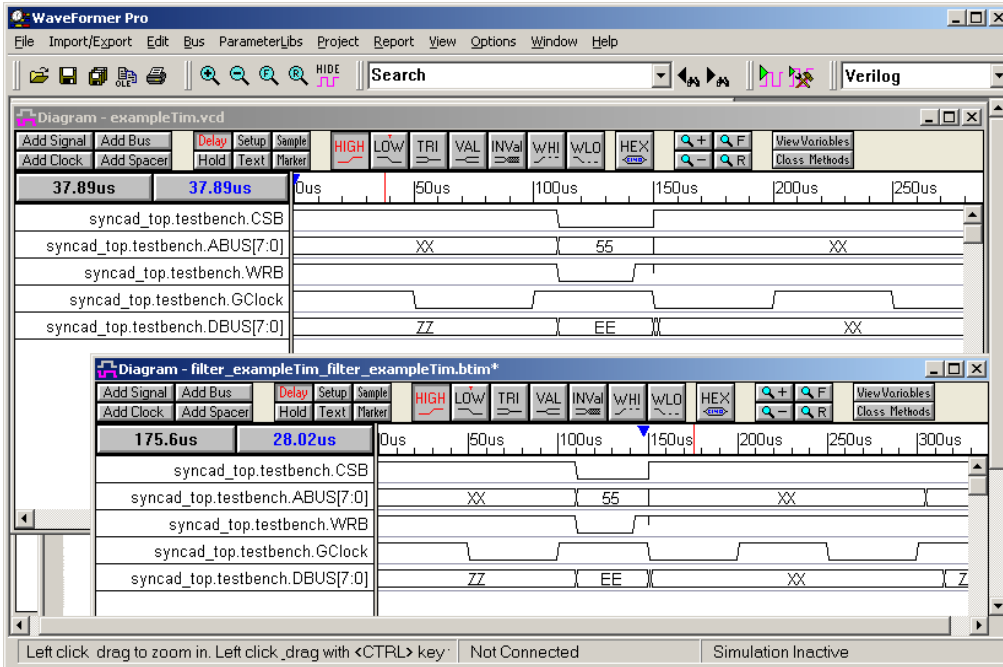


Nutzen eines Filters:

Menue: Optionen/Set Filter Diagram File



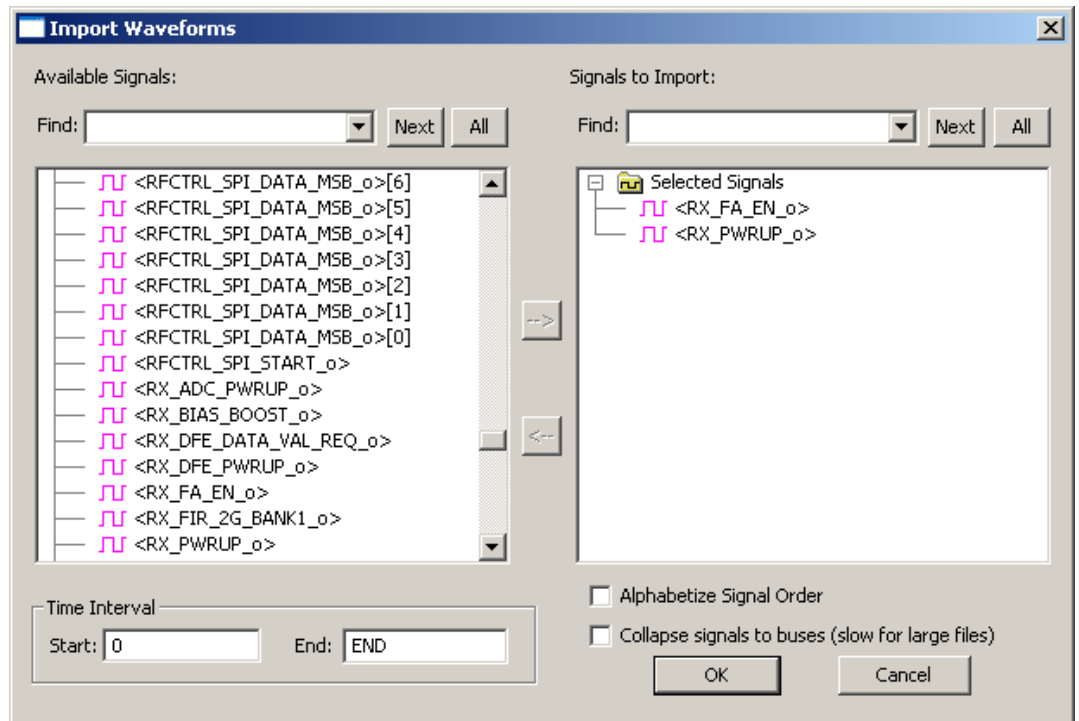
Resultat:



Nur die Signale, die im Filter Diagramm enthalten sind, werden importiert.

Vorteil:

Das Filter Diagramm ist wieder verwendbar. Sonst muss eventuell bei jedem Import derselben Datei immer & immer wieder ein Filter definiert werden.



Hinweis:

Nehmen Sie zum Importieren das Menue Import/Export & Import Timing Diagram From.
Der Hinweis in HELP ist etwas veraltet.

The screenshot shows the SynapticAD On-Line Help window. The left pane displays a tree view with 'VCD' selected. The main pane shows the following content:

13.7 Import List of Supported Simulators and Test Equipment

WaveFormer Pro can import waveform data from several simulators and pieces of test equipment. The importing of waveform data is accomplished in the File Open dialog by selecting the correct format using the Files of Type list box. The following scripts are supported:

Standard Formats:

- **Verilog Change Dump**, reads VCD waveform files generated by Verilog simulators.
- **Test Vector Spreadsheet** reads waveforms generated inside spreadsheets.
- **VHDL Waves Vectors** reads waveform files generated by VHDL simulators.

Simulators:

- **AWF** reads AWF files
- Altera Table Format reads TBL files.
- **DesignWorks** accepts waveform files generated by Capilano Computing's DesignWorks' gate-level simulator.
- **PeakVHDL** reads AWF waveforms generated by Accolade's PeakVHDL and PeakFPGA simulators.
- **Protel Advanced PLD** reads waveforms generated by Protel Technology's Advanced PLD simulator.
- **SpeedWave** reads waveforms generated by Viewlogic's SpeedWave VHDL simulator.
- **Spice** reads CSD and TR files from.
- **TimeMill** reads CSV waveforms generated by Synopsys's TimeMill.
- **Workview WFM** reads waveform files generated by Viewlogic's Viewsim simulator.

Timing Diagram Formats:

- **Binary Timing Diagram** reads BTIM files that contain all signal and parameters as well as project options in a fast binary file format. This is the default file format for all timing diagrams.
- **Timing Diagram** reads TIM files that contain all signals and parameters as well as project options settings in ASCII based format. This was the standard format used for all timing diagrams before version 8.0 of the SynapticAD product line. This format is still support but is slower loading than the btim format.
- **TDML** reads timing diagram files create using the Timing Diagram Markup Language.
- **TimingDesigner** accepts TD timing diagram files from Forte Design's timing diagram editor.
- **Text Free Parm** accepts txt files used for editing libraries saved in the standard spreadsheet compatible file format: comma and tab separated text files that contain a header (first line must be "NAME, MIN, MAX, COMMENT"). This is also compatible with Chronology@ library files (formulas are only supported in the td files).
- **Free Parm** reads FP files with just the free parameters information in Timing Project File Format. This is one way to make a library file.

Test Equipment Formats:

- **Agilent Infinium Digitizing Oscilloscope**
- **Agilent Logic Analyzer** reads HPL data files